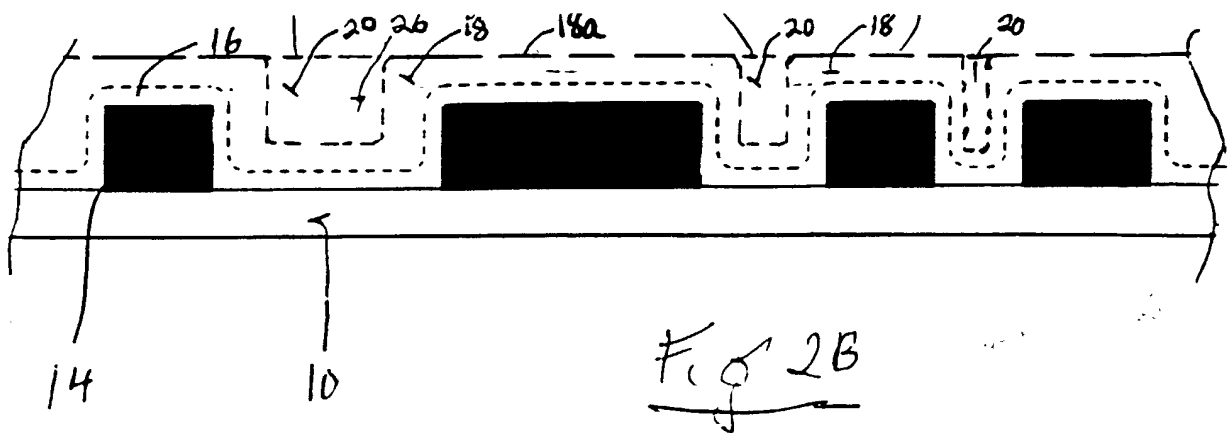


**Figure : After deposition of gapfilling polymer and CMP**



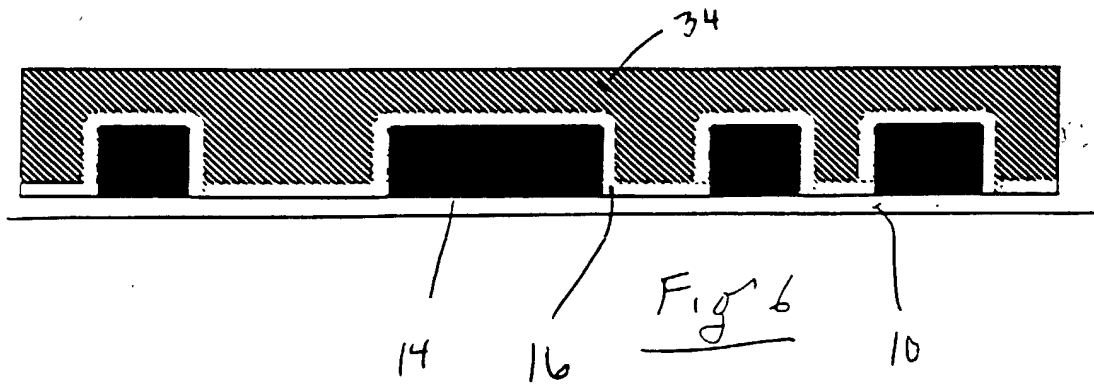
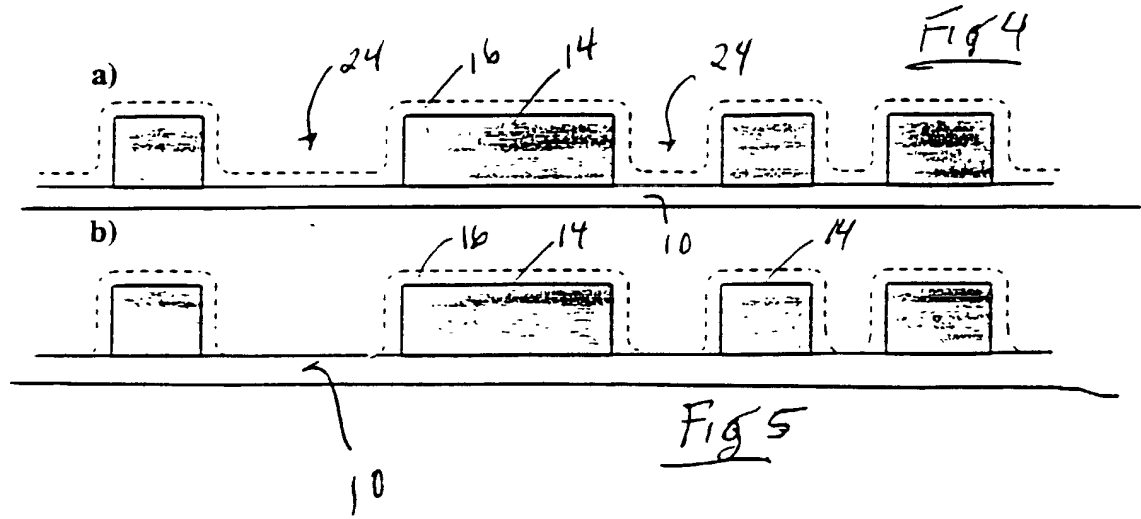


Fig 7

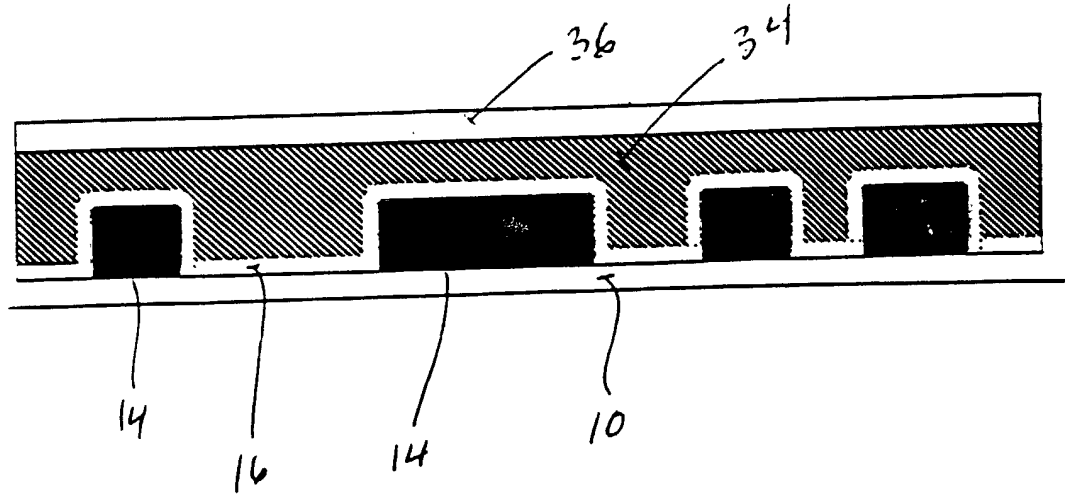
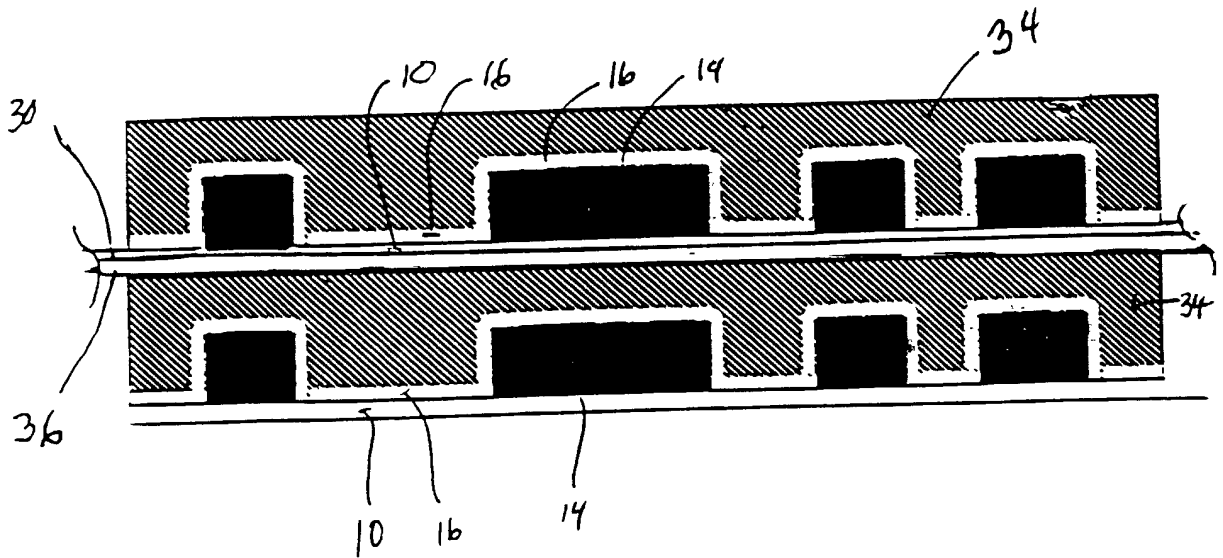
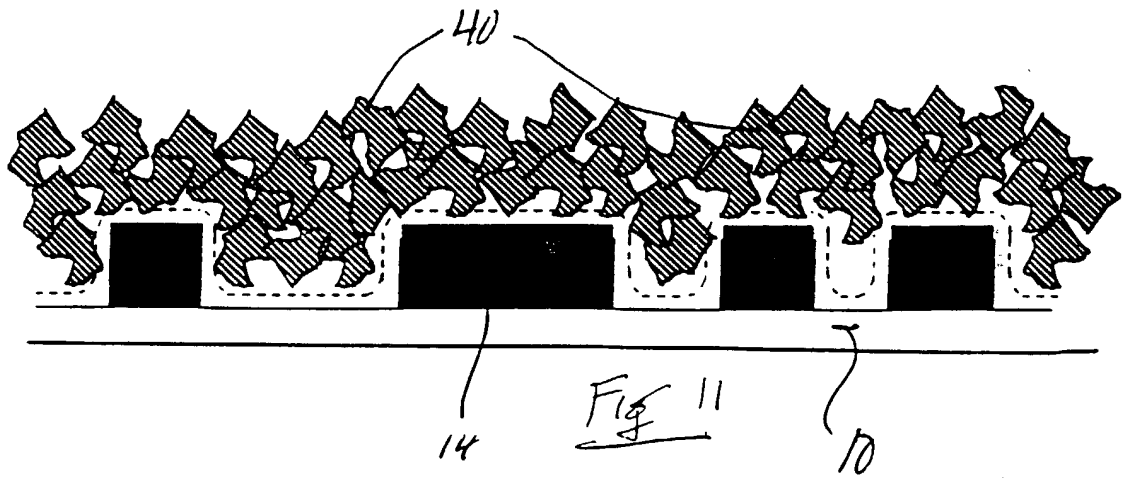
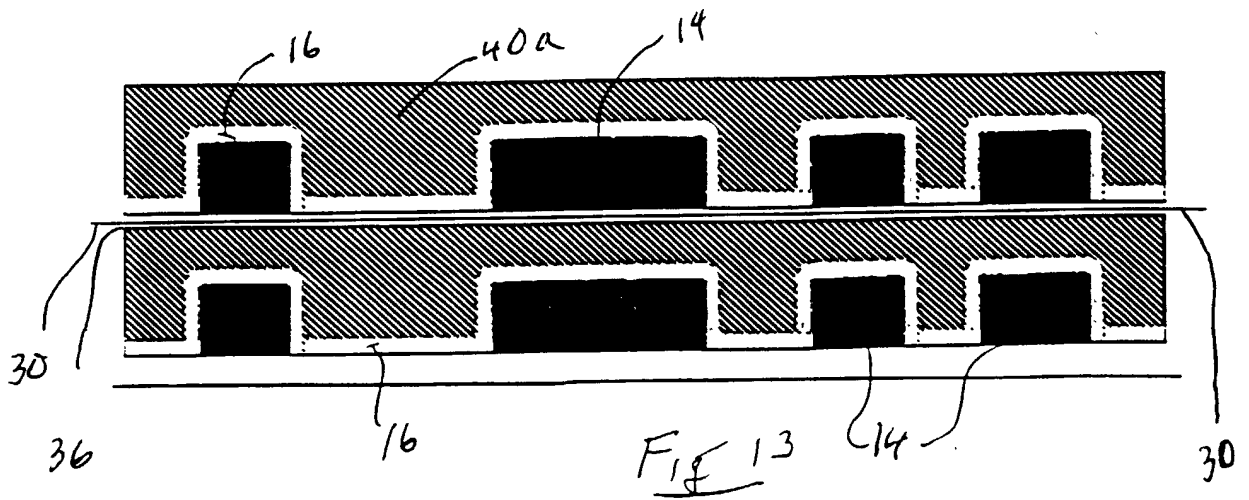
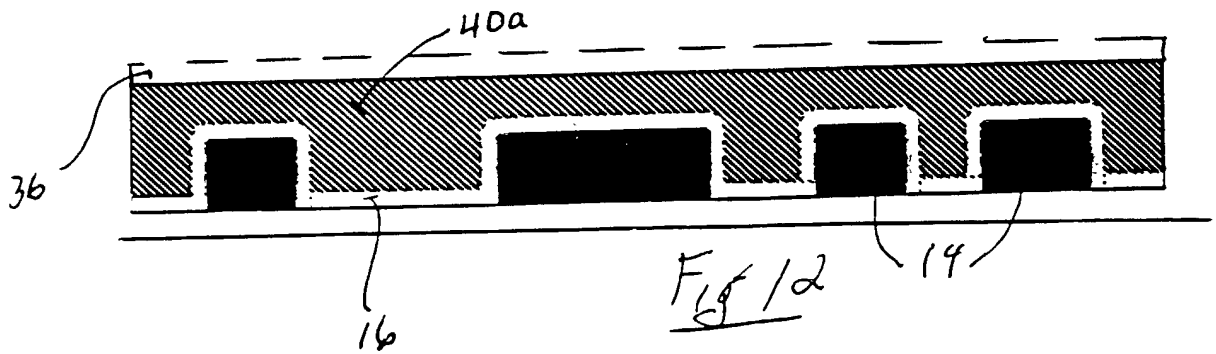


Fig 8







Fr 14

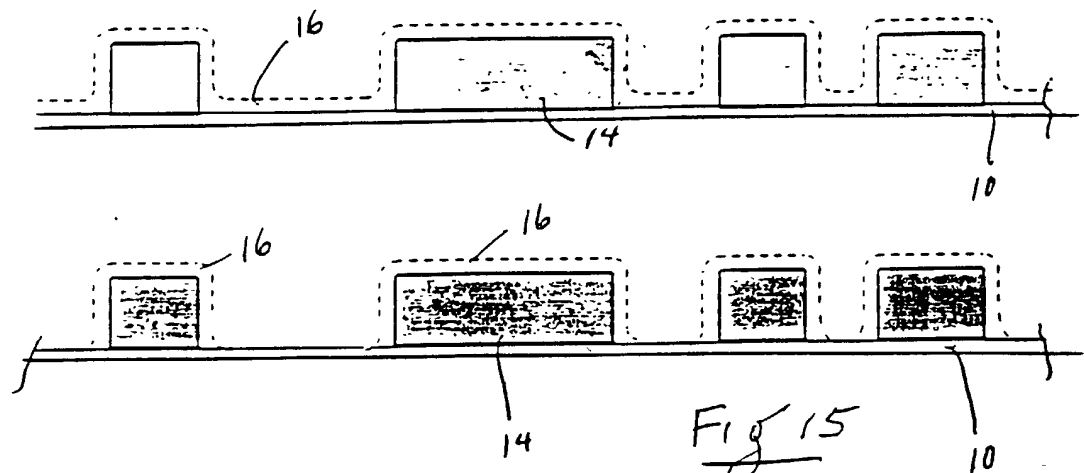
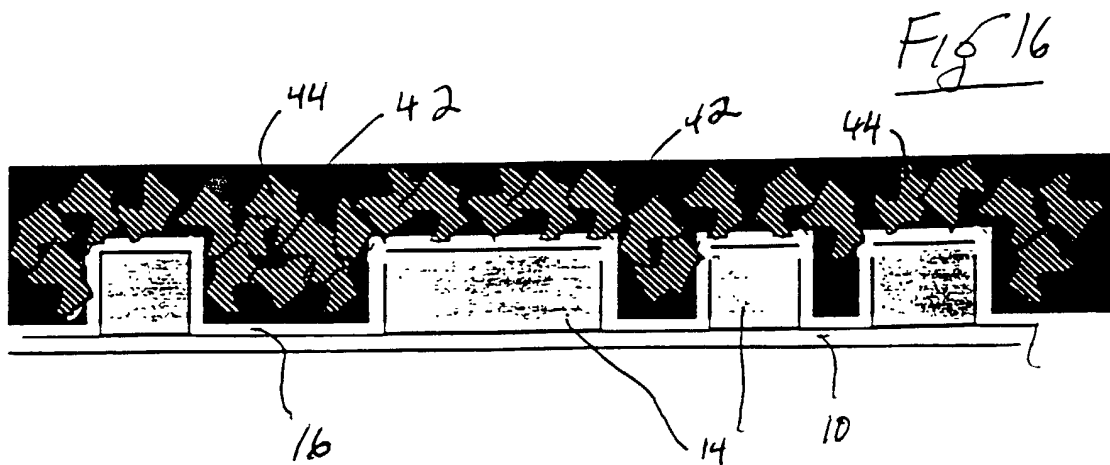


Fig 15



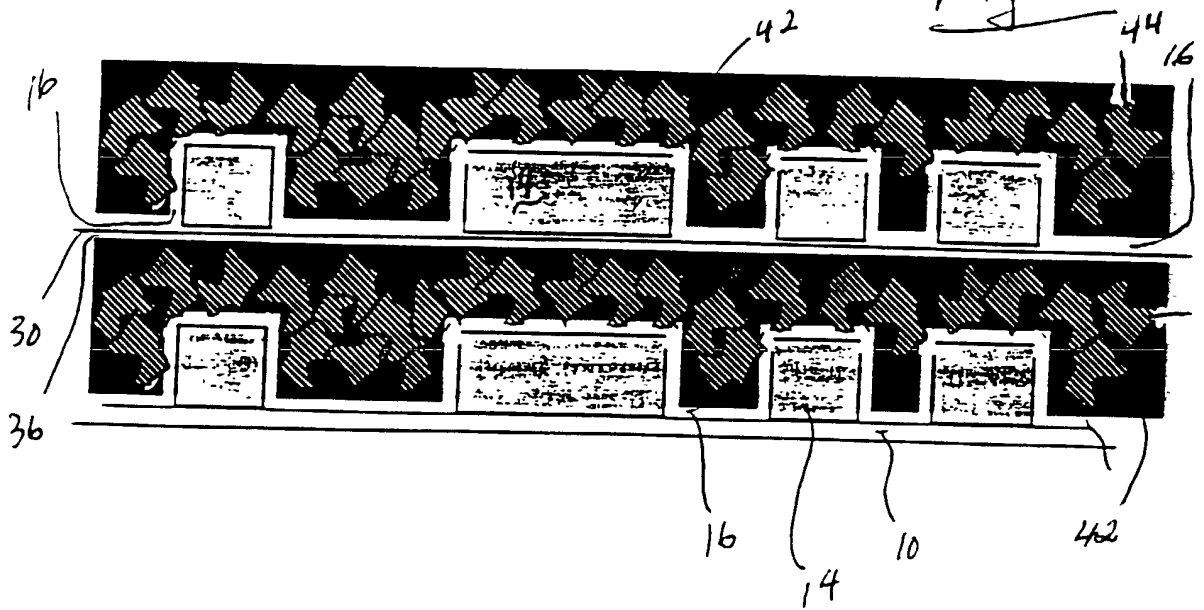


Fig 18

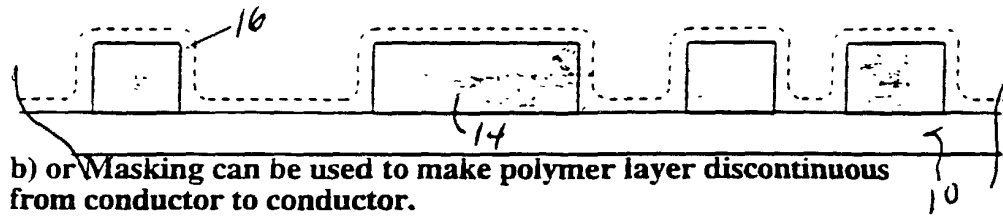
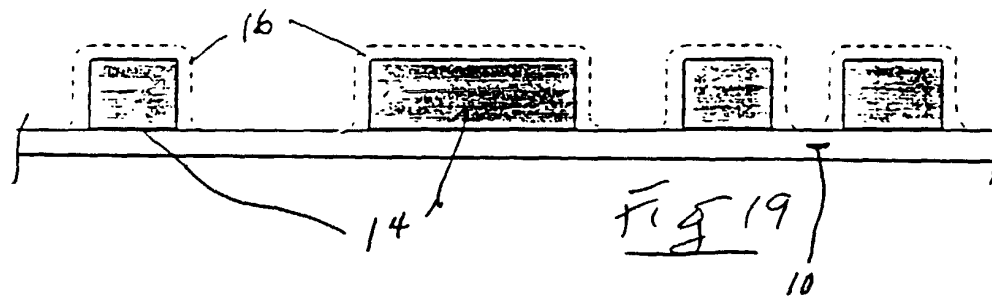


Fig 19



50

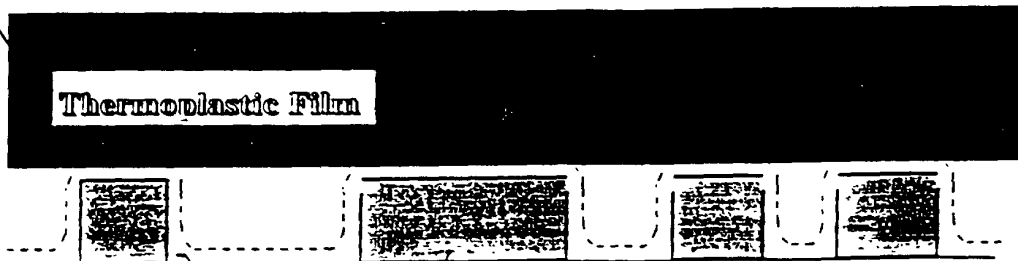


Fig 20

16

14

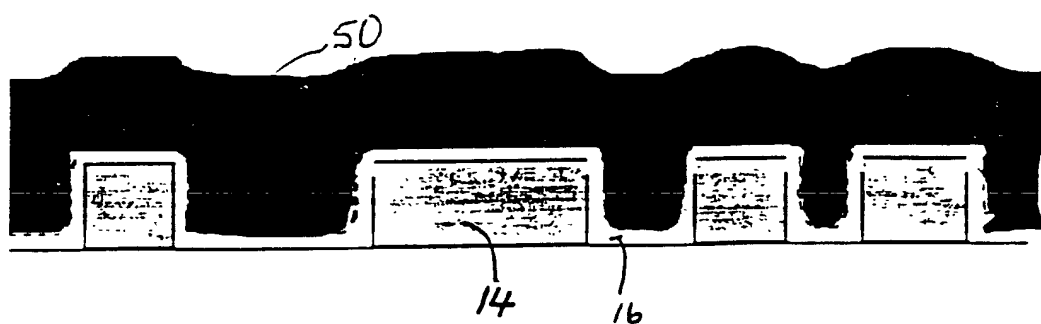
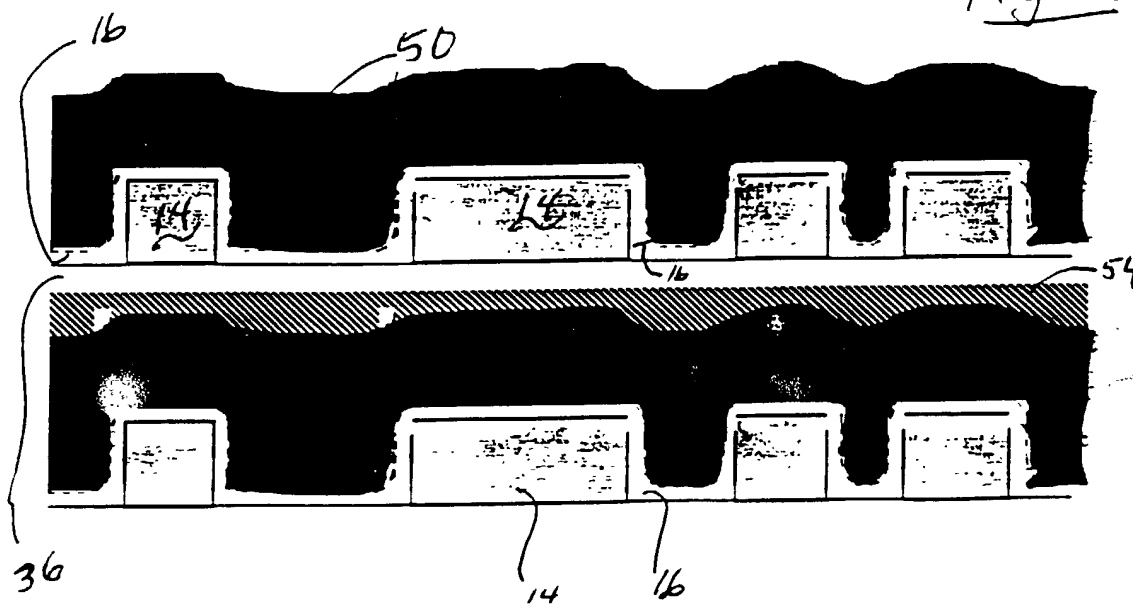
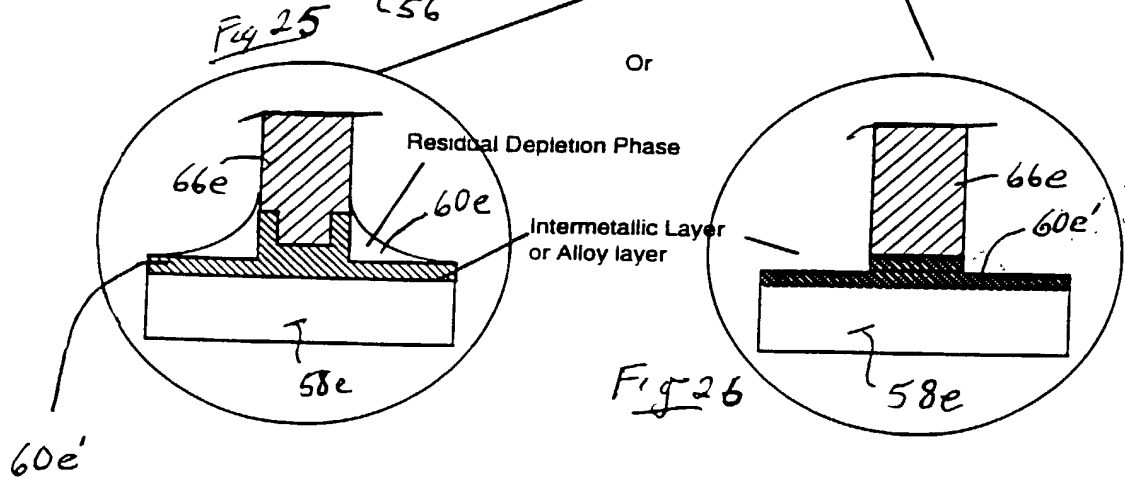
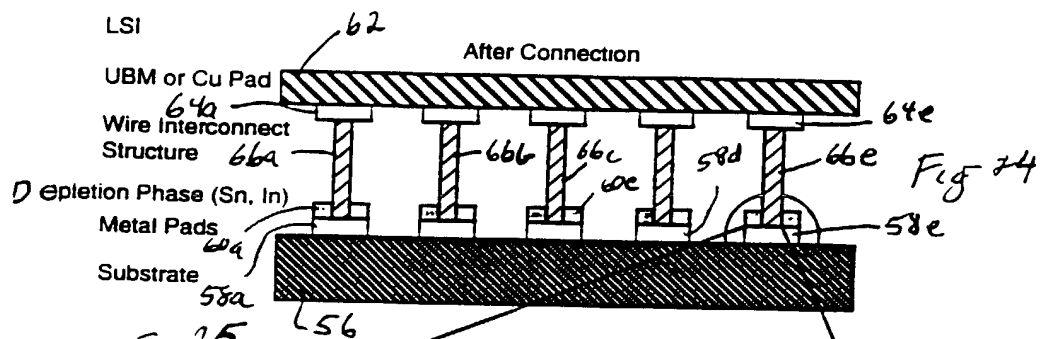
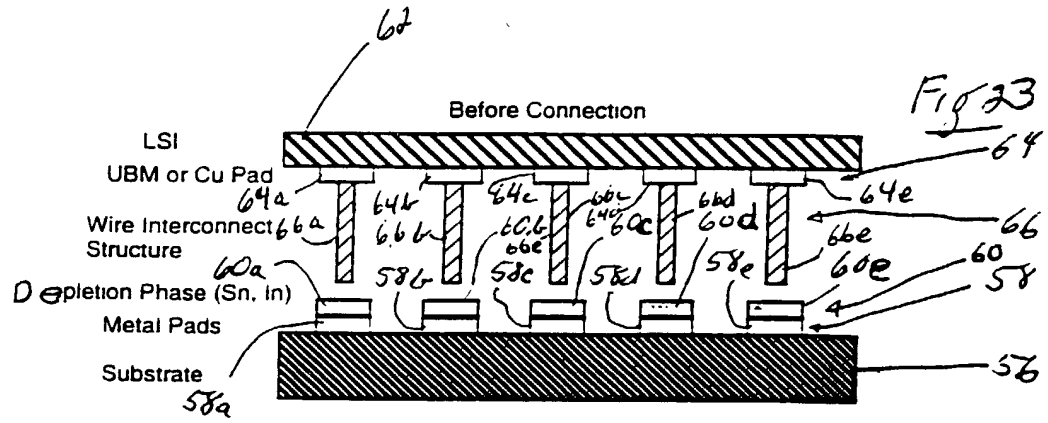


Fig 22





Structure 2

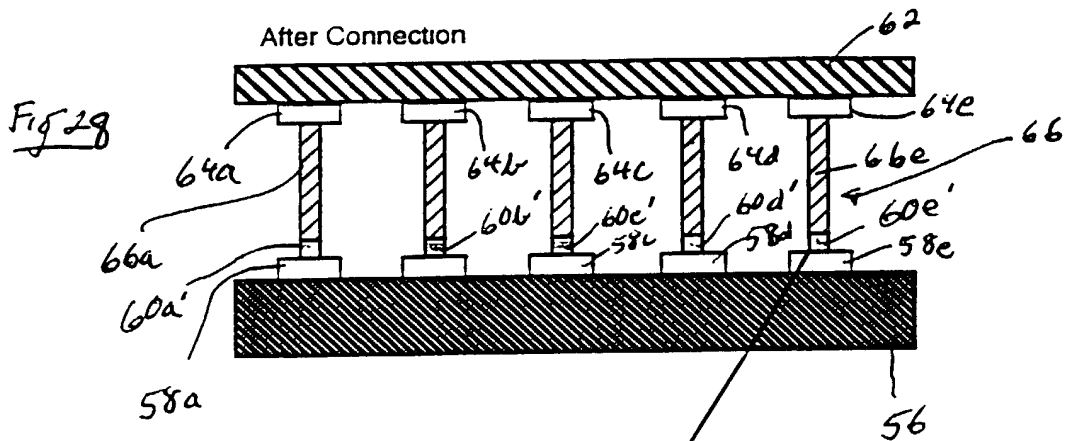
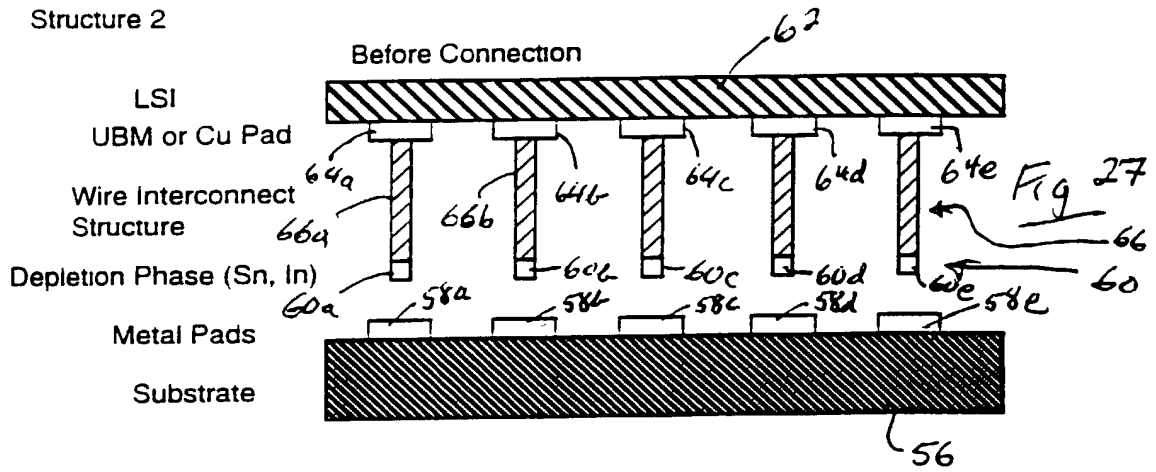
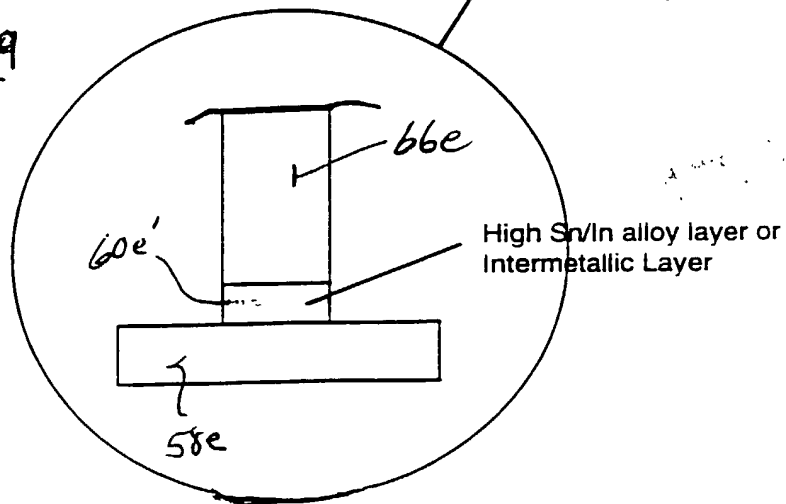
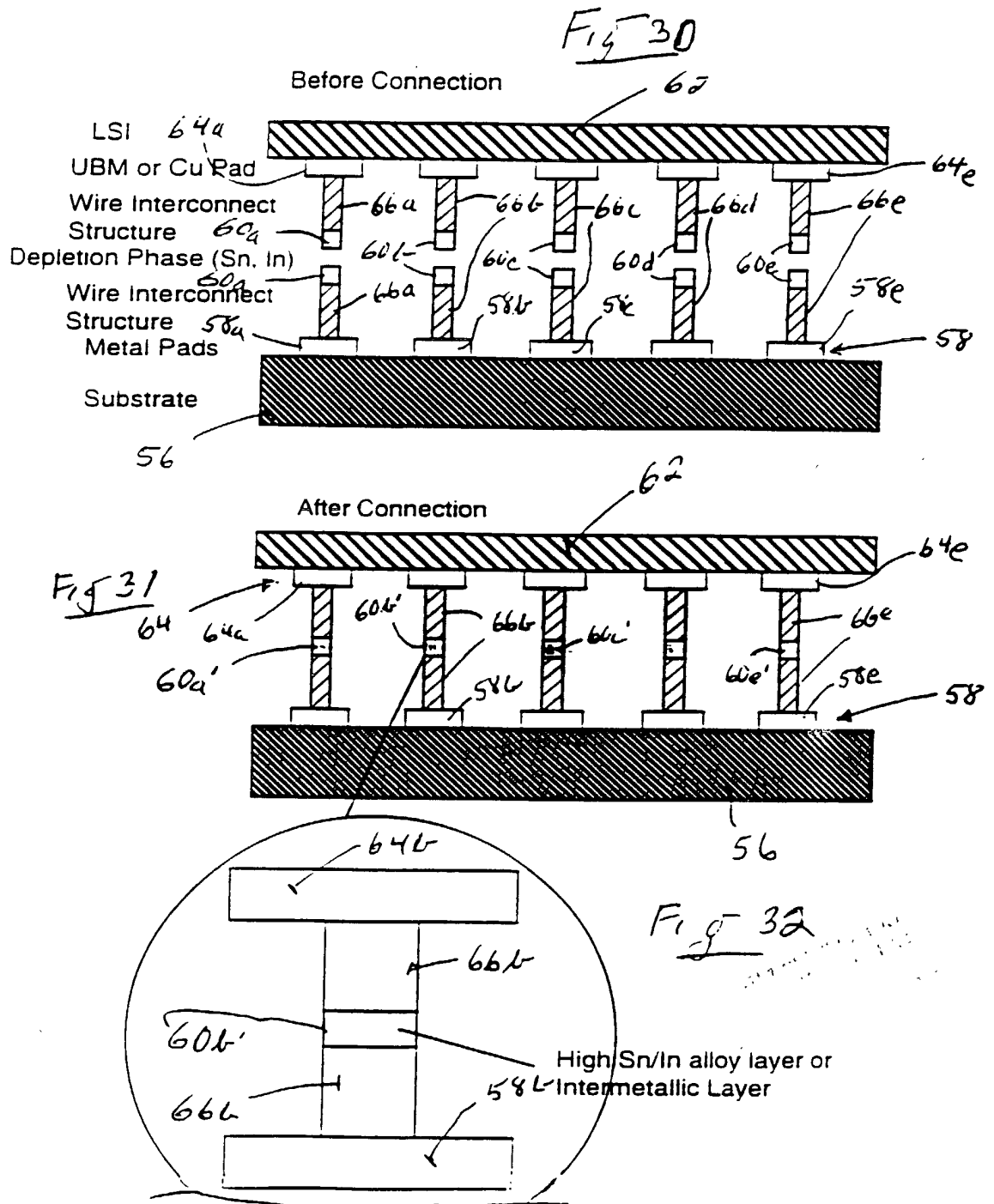


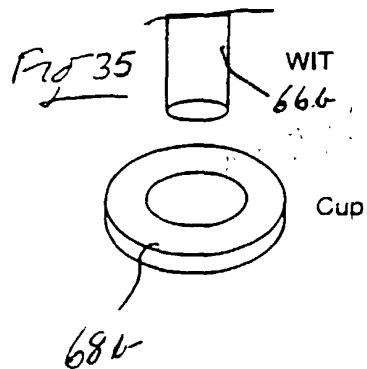
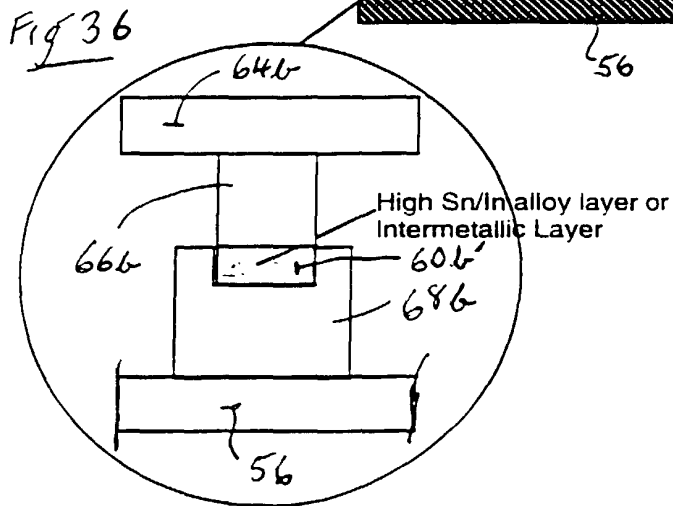
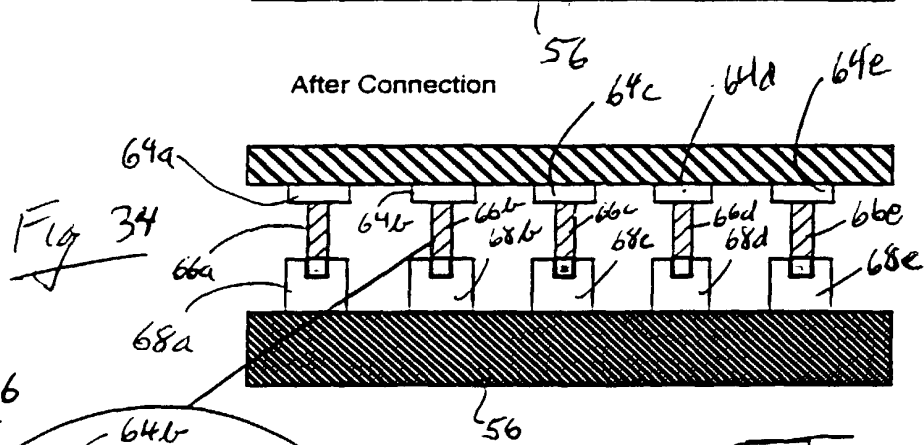
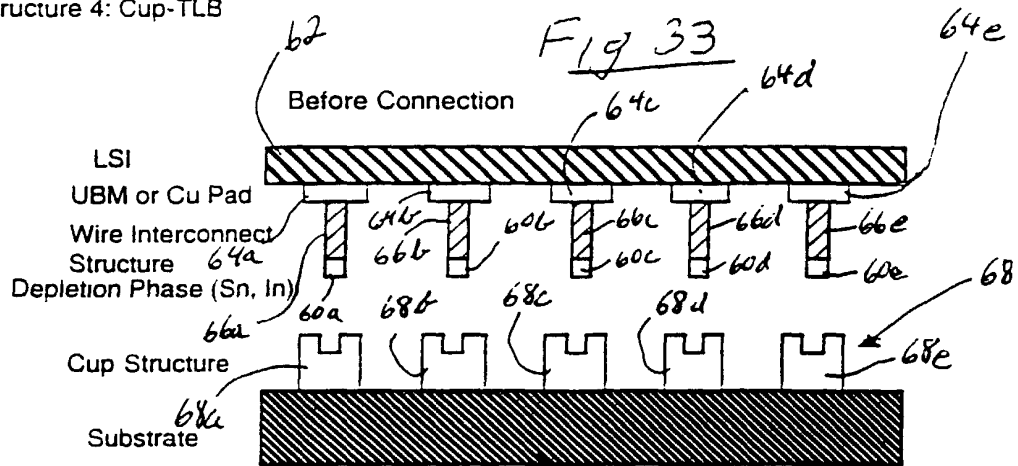
Fig 29



Structure 3



Structure 4: Cup-TLB



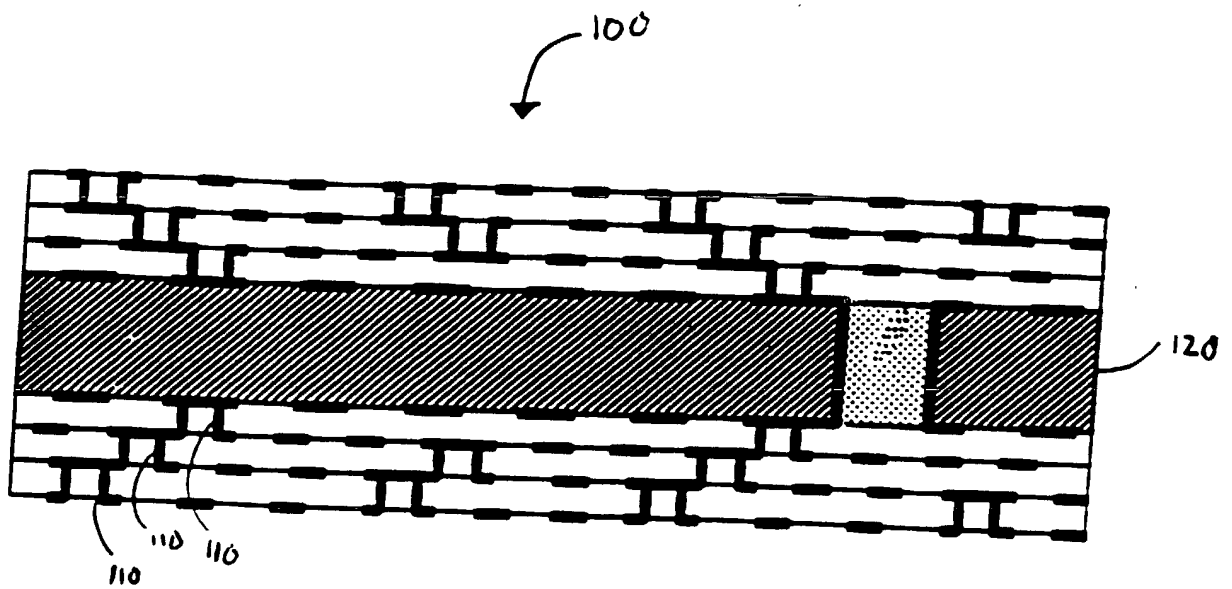
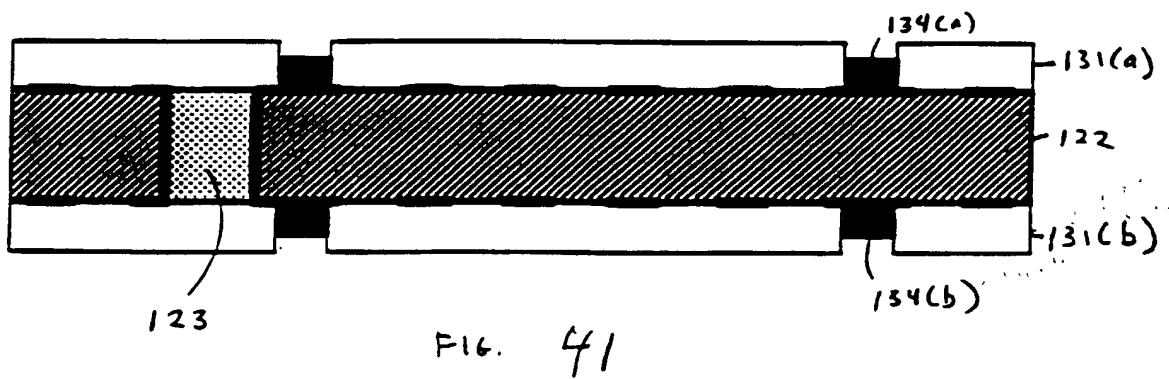
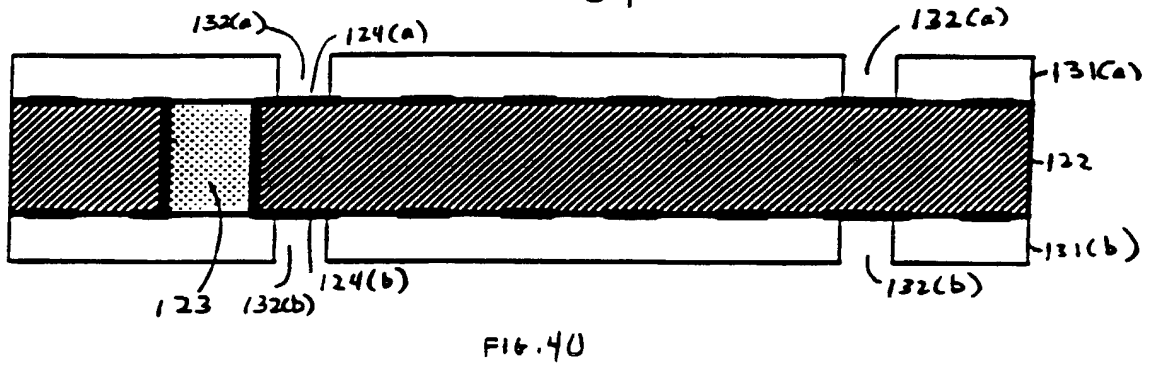
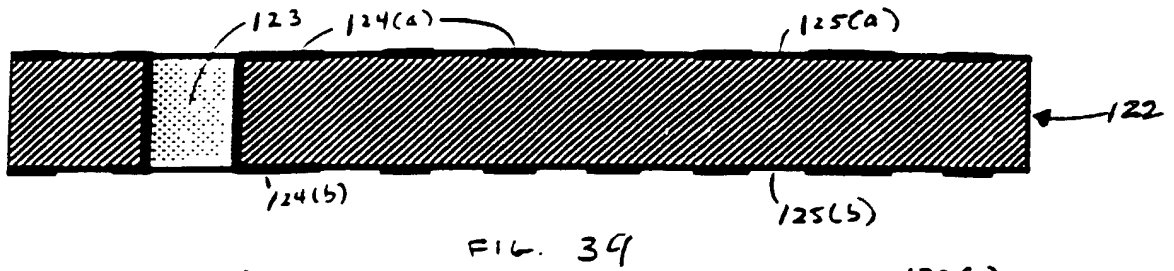
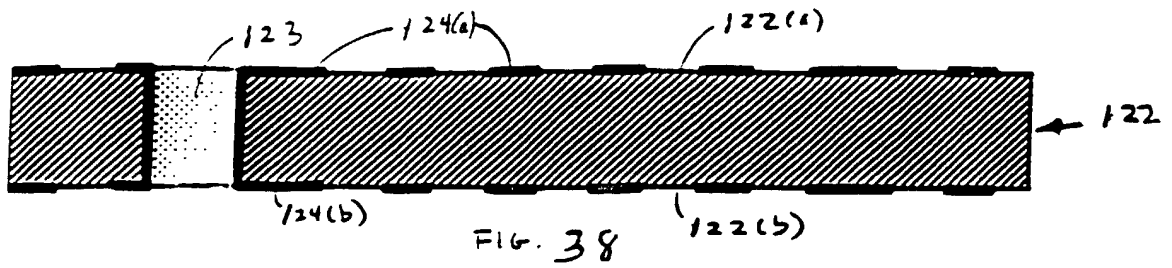


FIG. 37



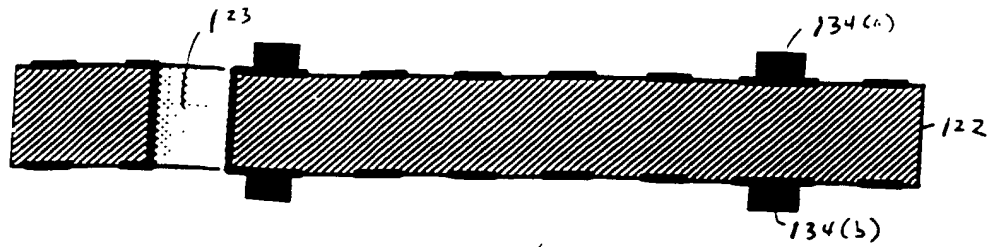


FIG. 42

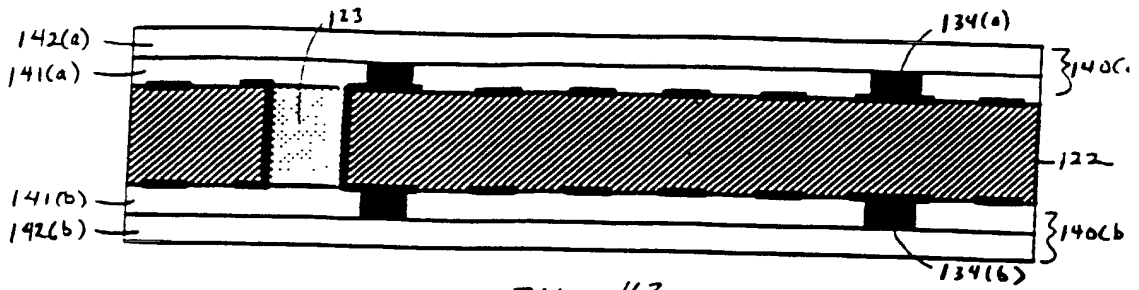


FIG. 43

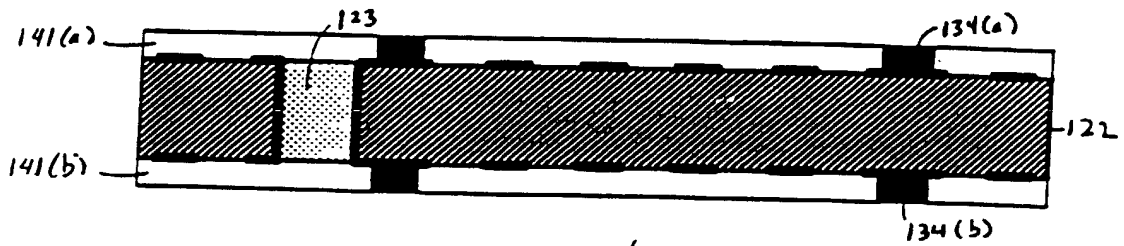


FIG. 44

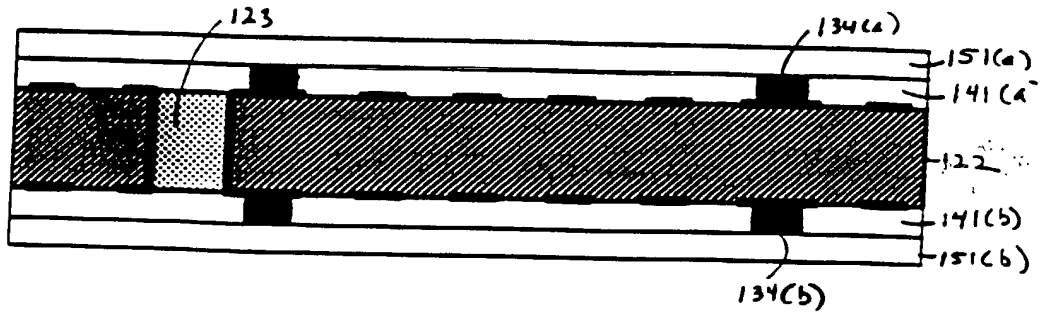
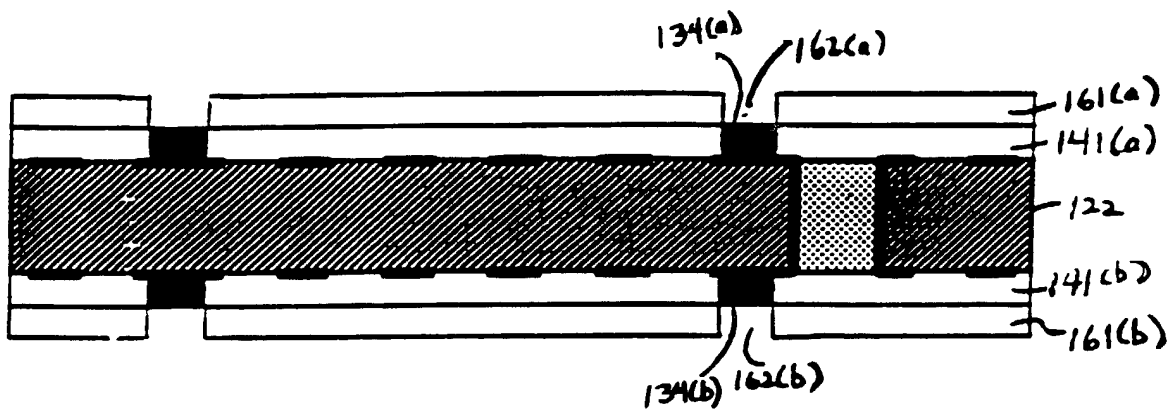
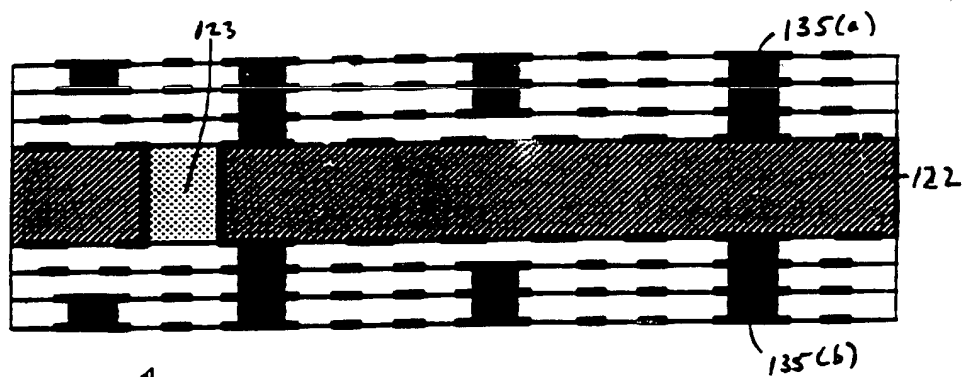
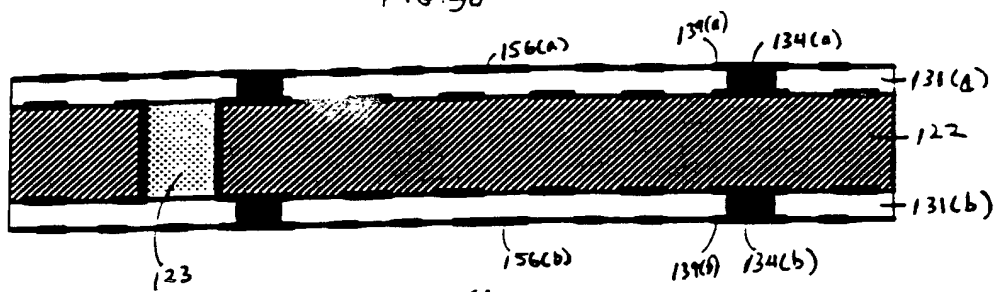
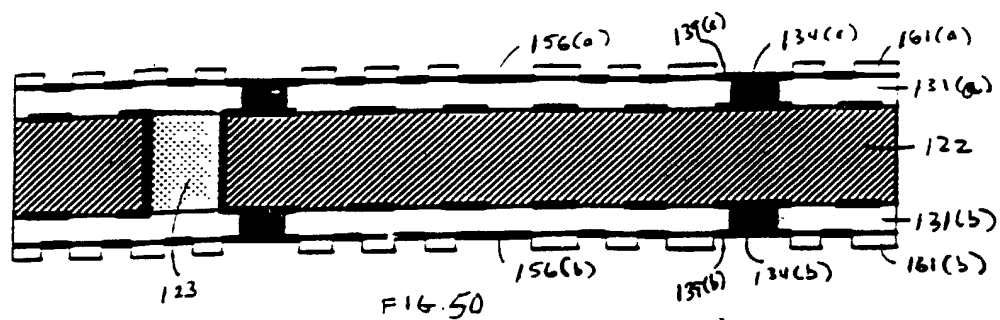


FIG. 45

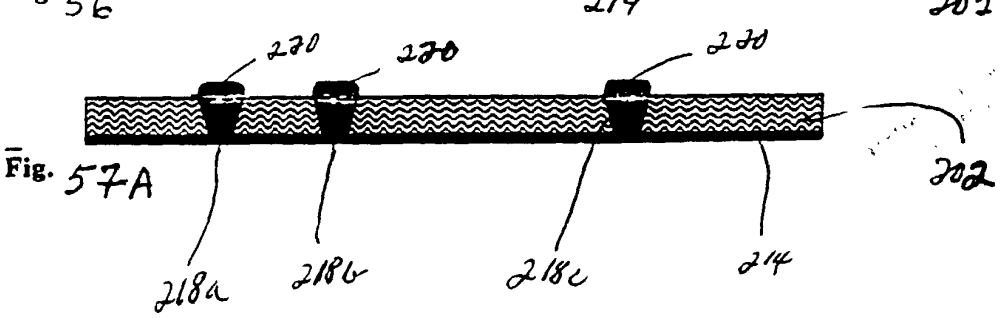
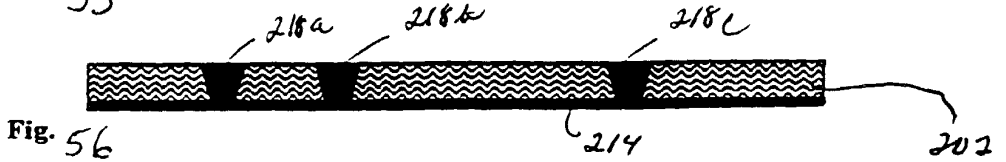
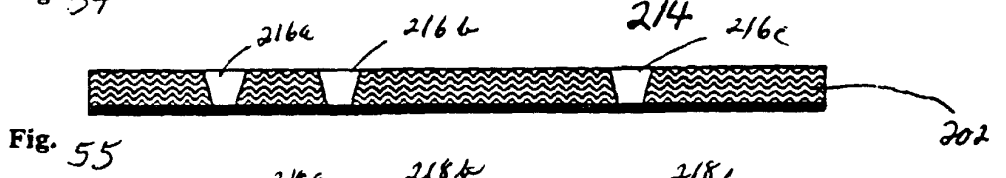
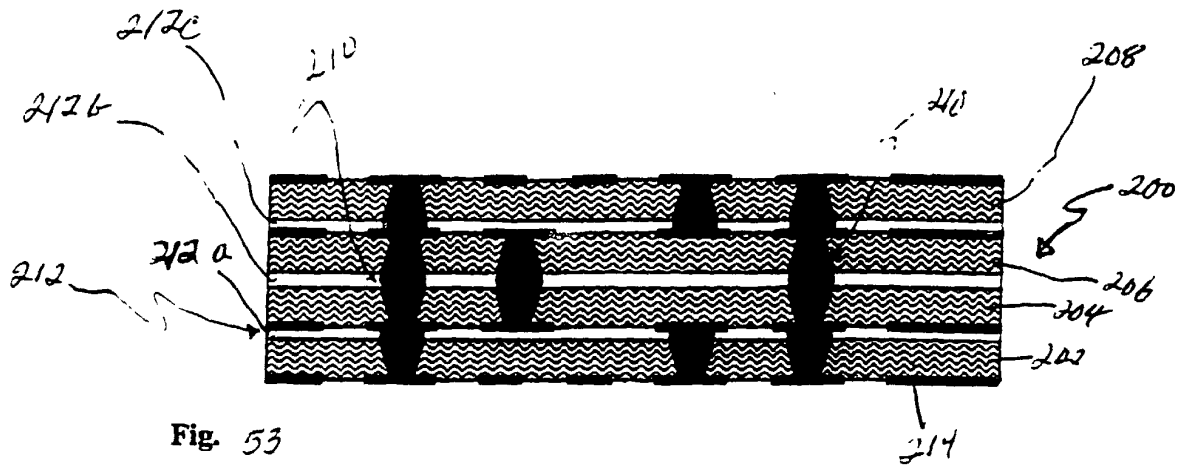


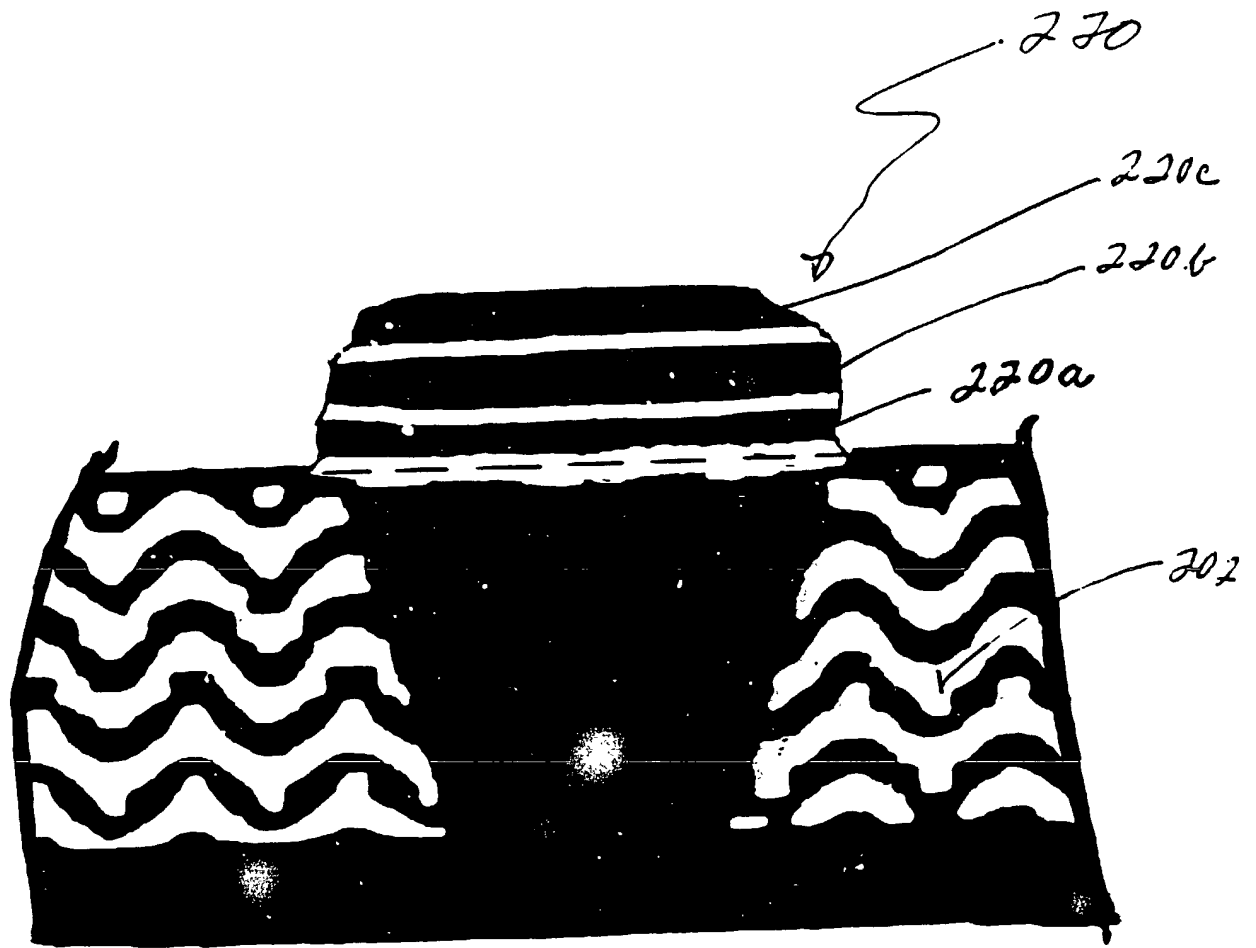
F16. 46





170

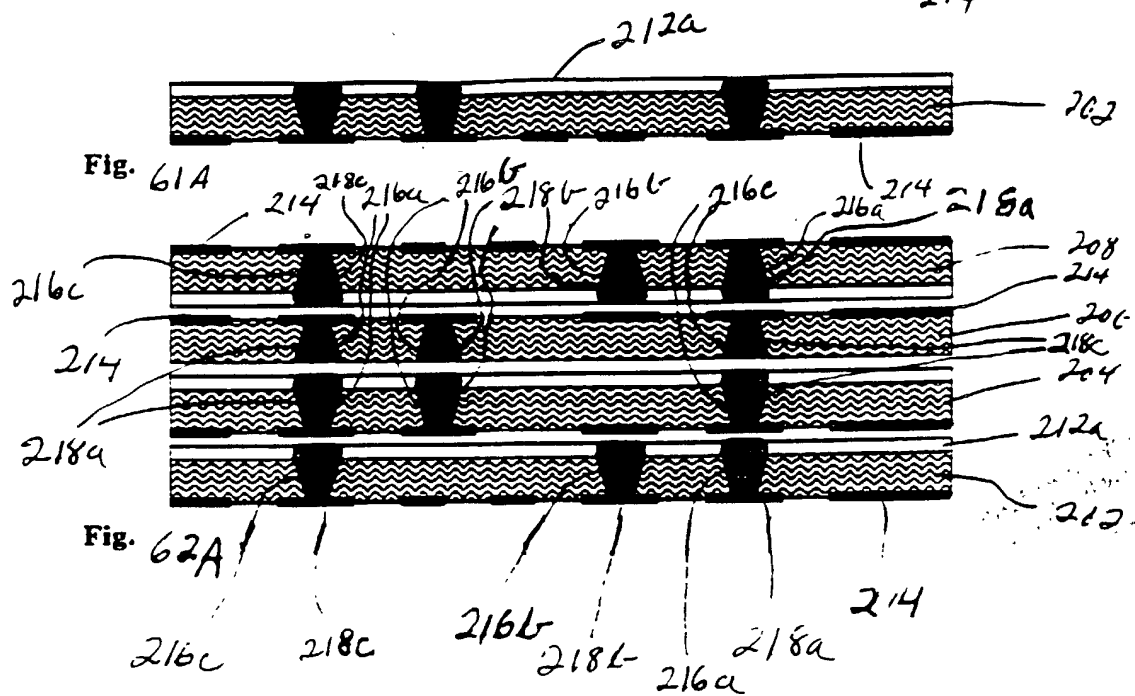
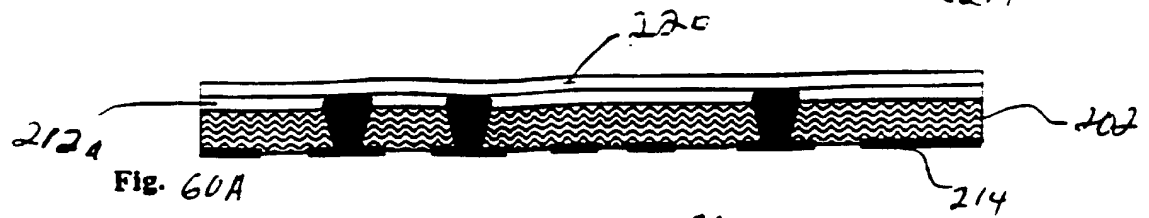
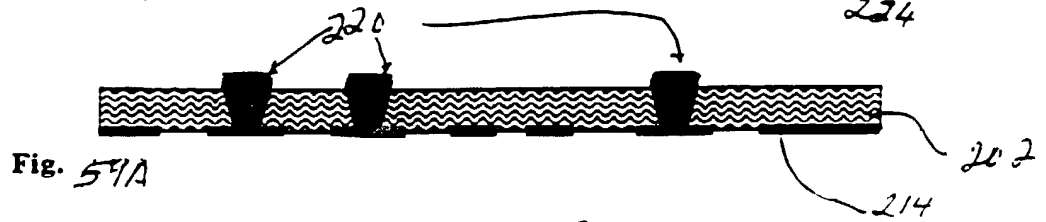
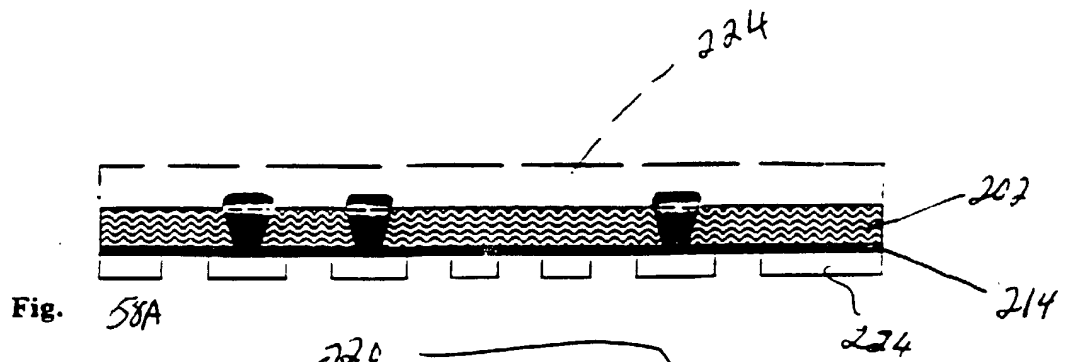


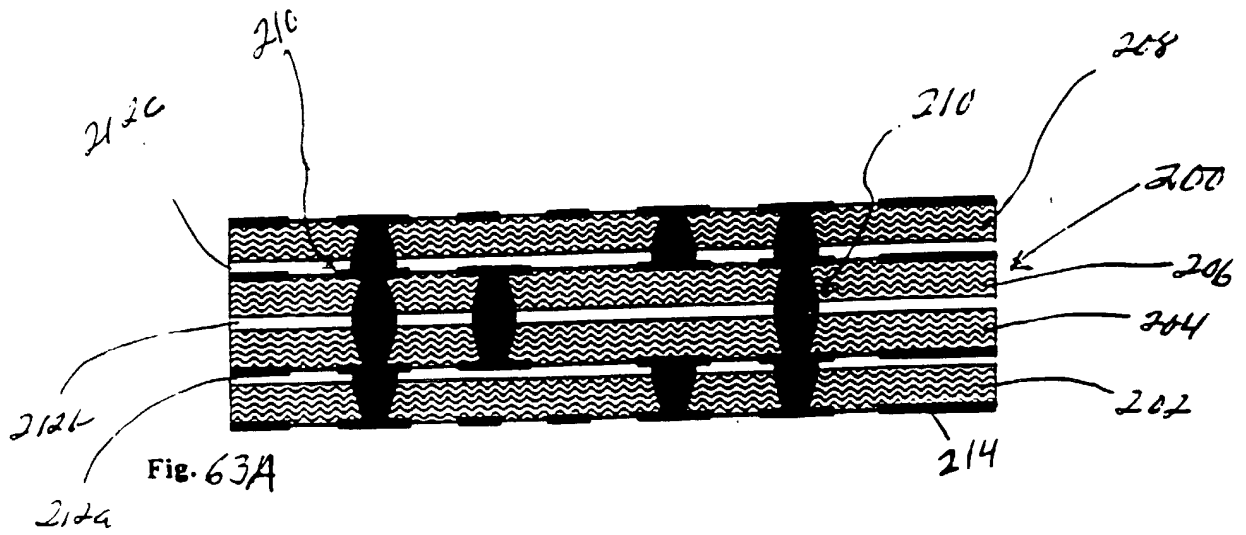


57B

A schematic diagram of a device 202, likely a battery or capacitor, showing a top layer 220b, a middle layer 220a, and a base layer 220. The device is mounted on a substrate 202 with a wavy pattern.

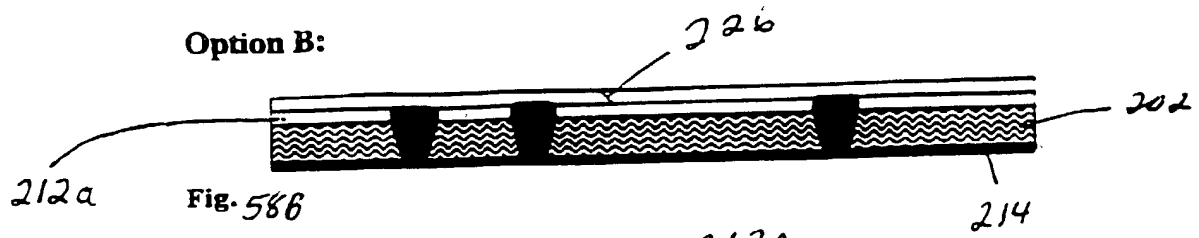
57 C



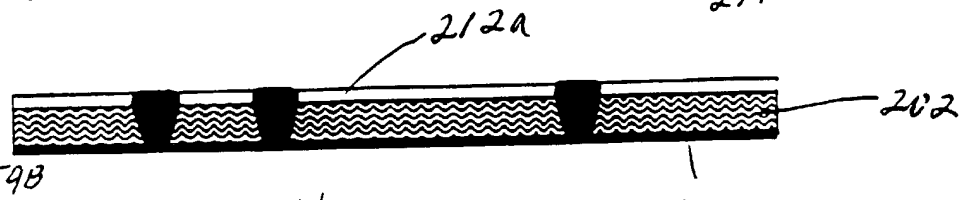


**Fig. 63A**

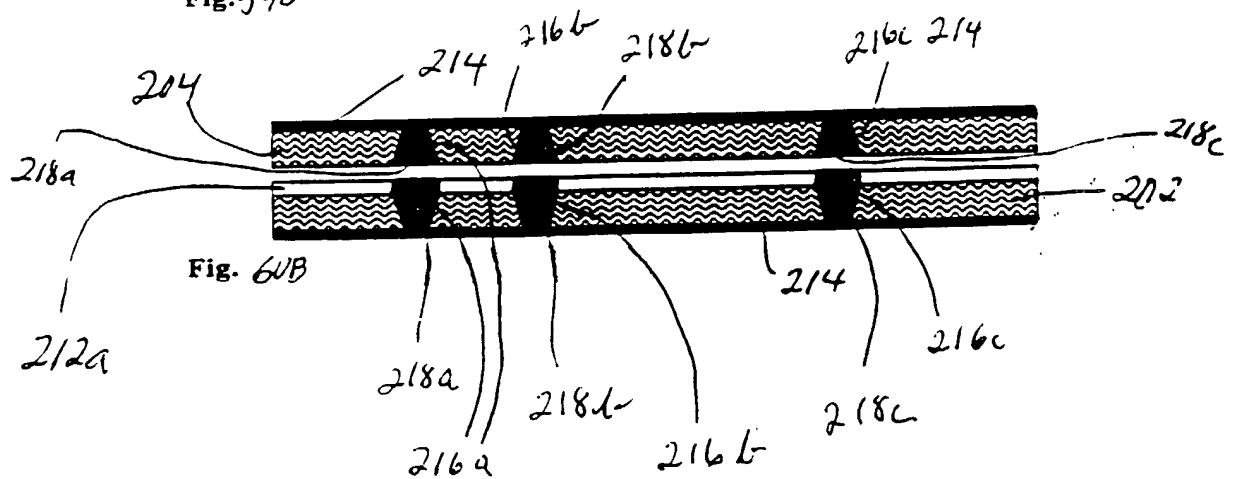
**Option B:**



**Fig. 586**



**Fig. 59B**



**Fig. 6VB**



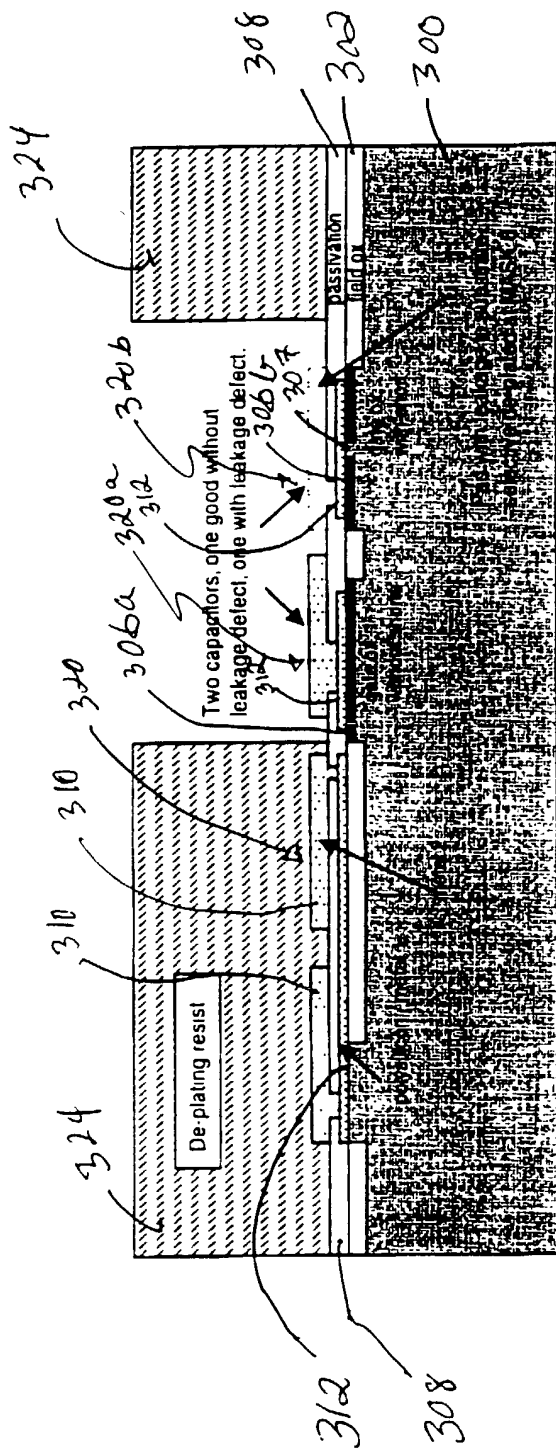
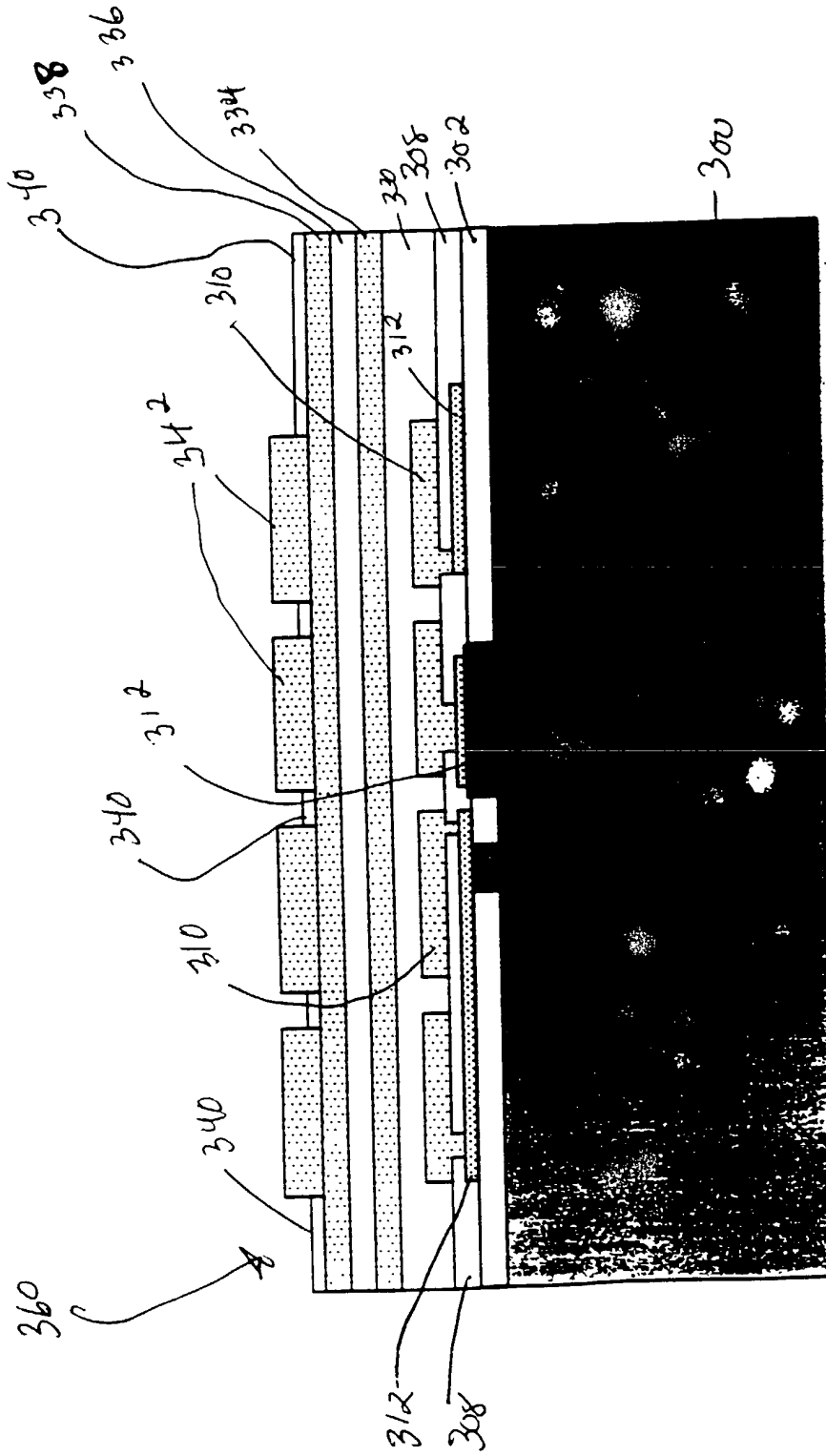
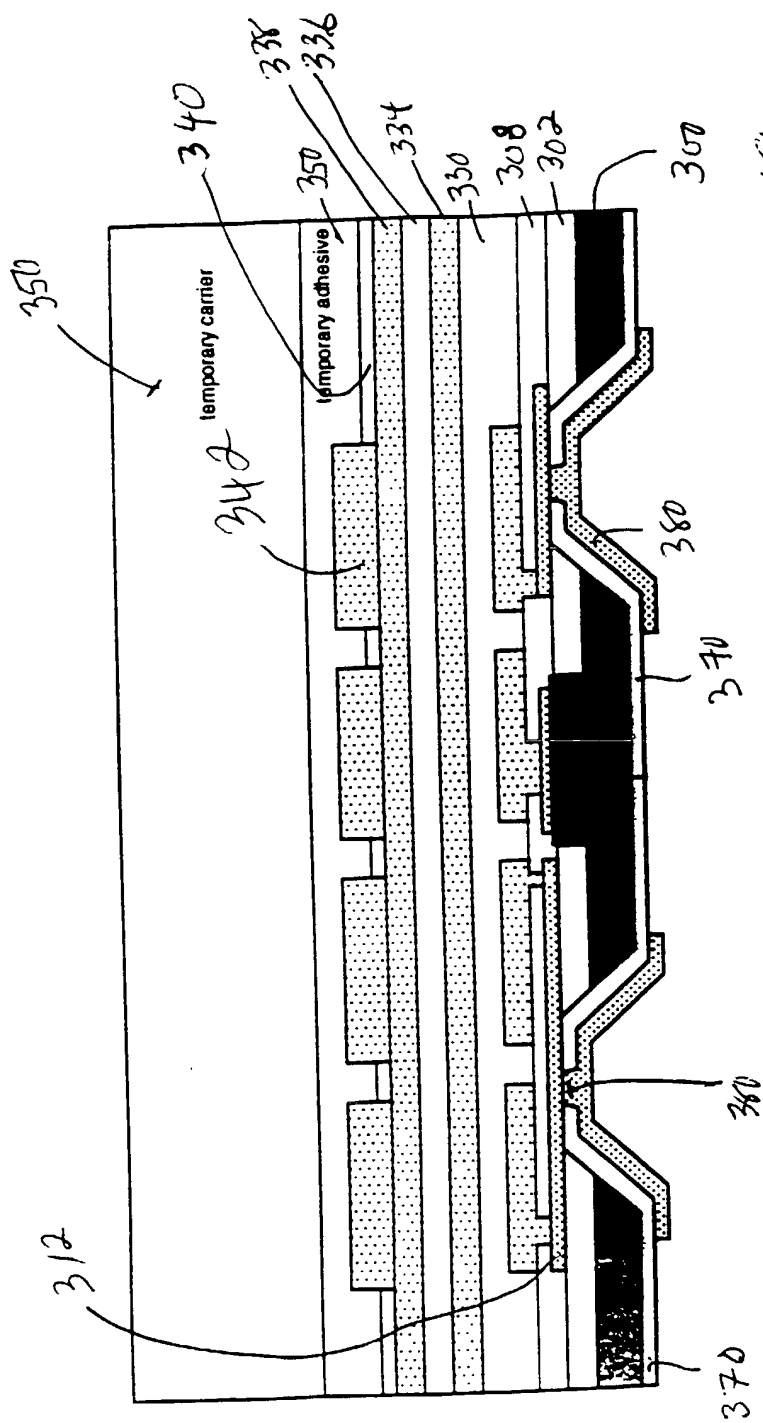


Fig 66





89-68



F1869

[illegible]

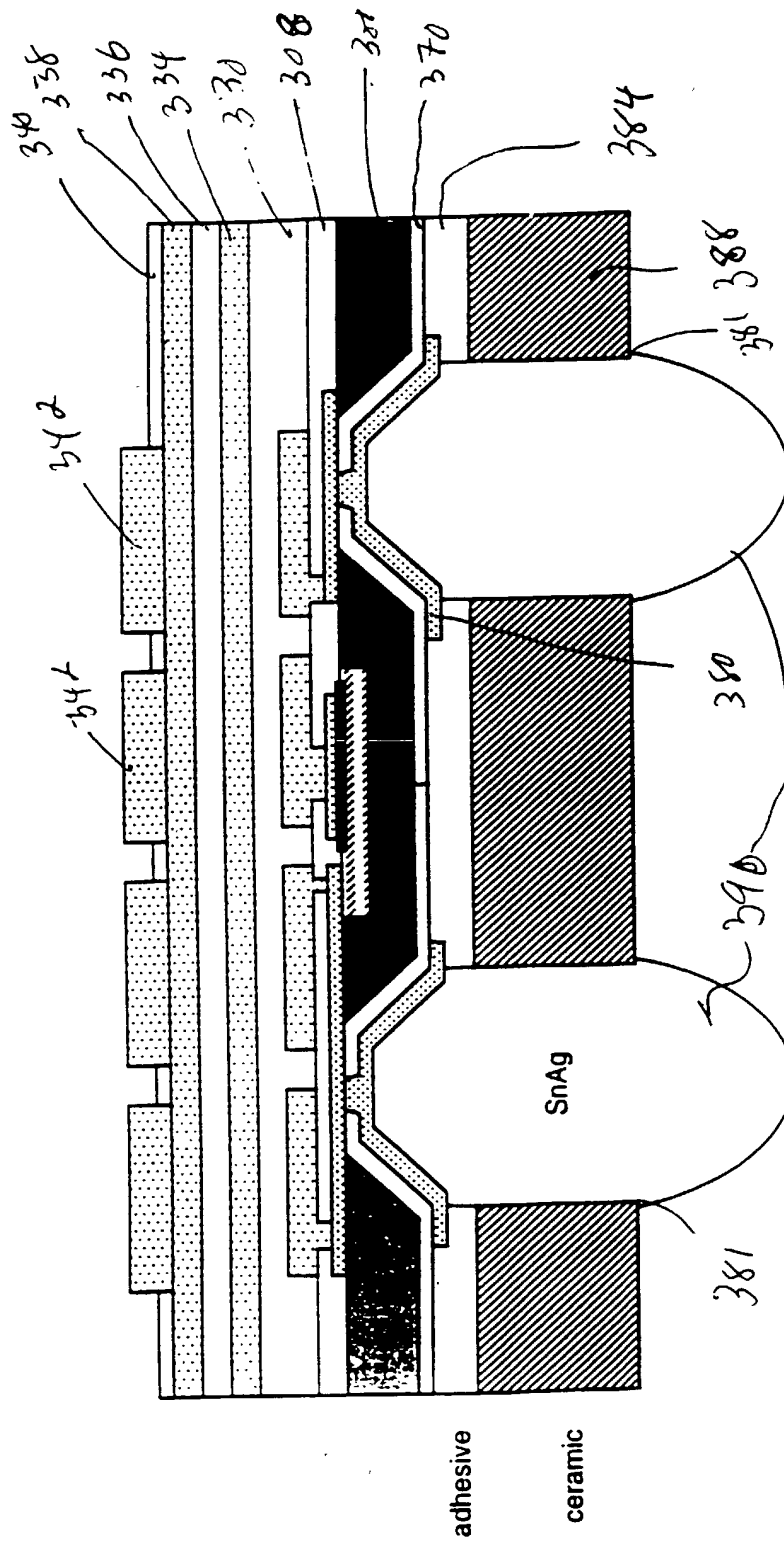


Fig 70

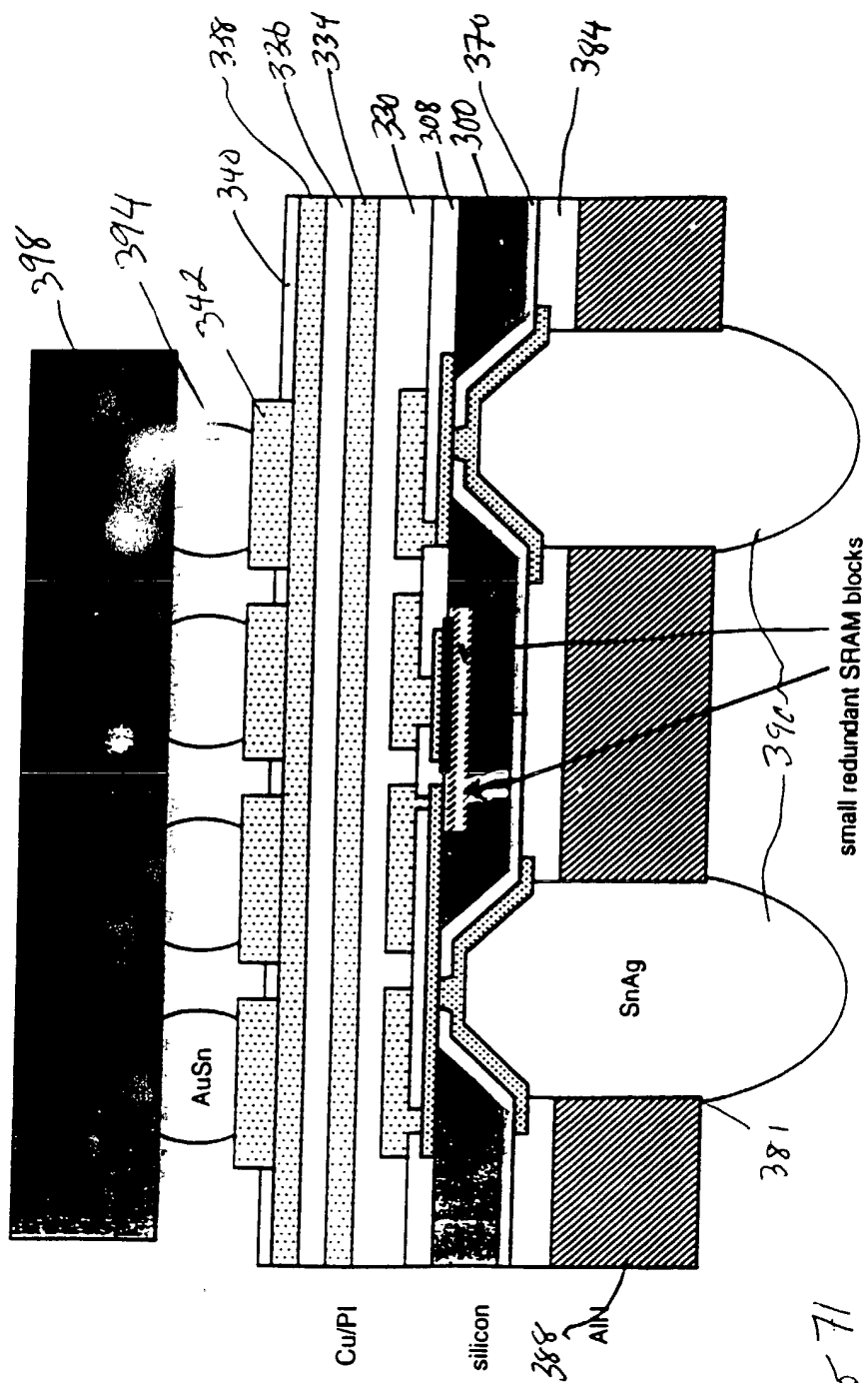
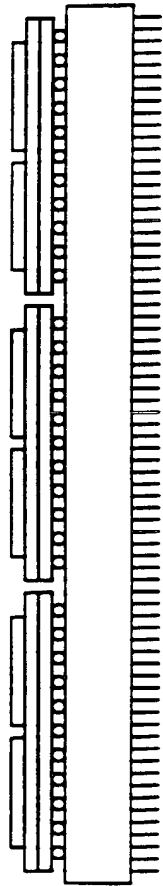
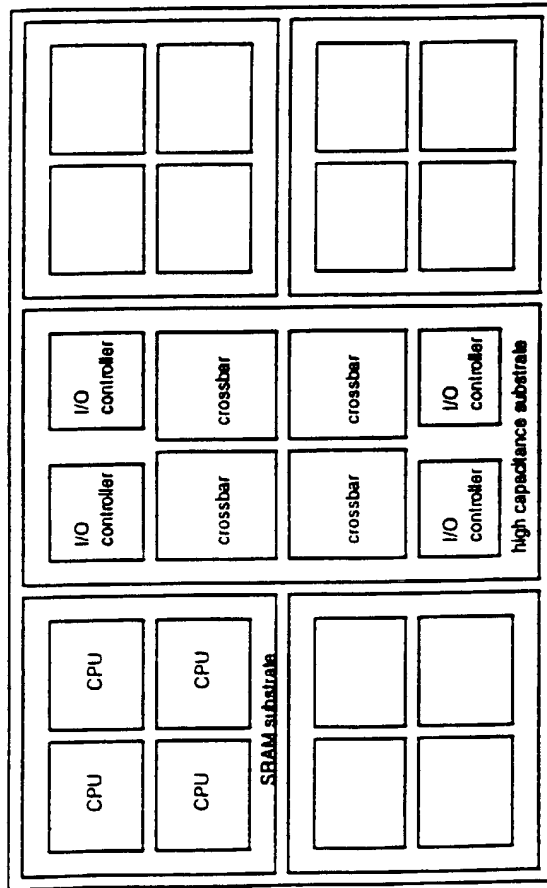


Fig 71

3D SuperChip  
Conventional MCM  
*Fig 72*



30x40mm  
12 per 6" substrate



60x120mm  
2 per 6" substrate  
*Fig 73*

00000000 00000000

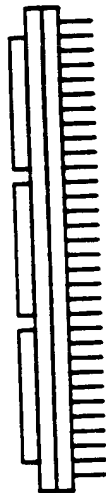
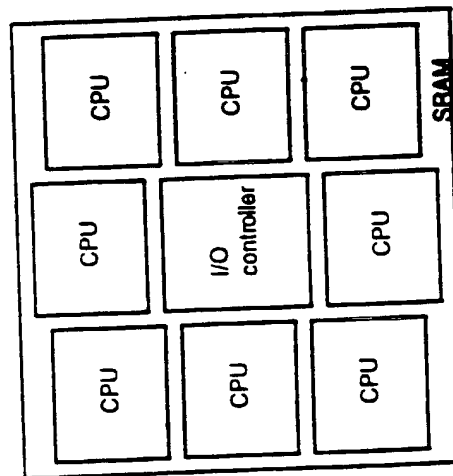


Fig 74

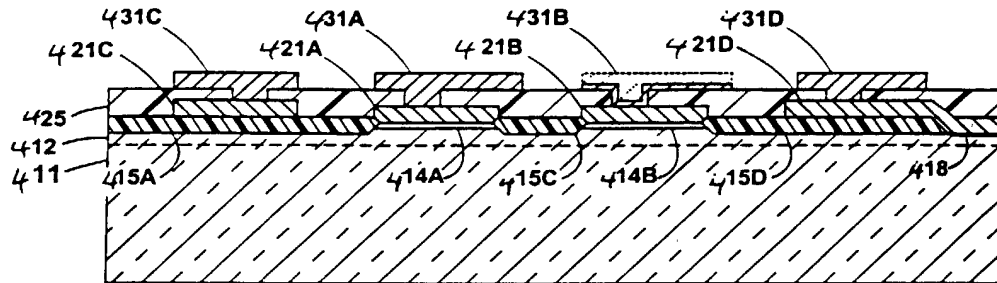


**50 x 50 mm**

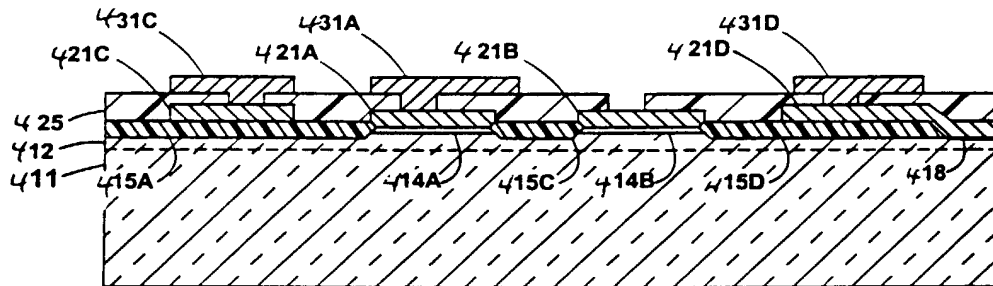
Fig 75



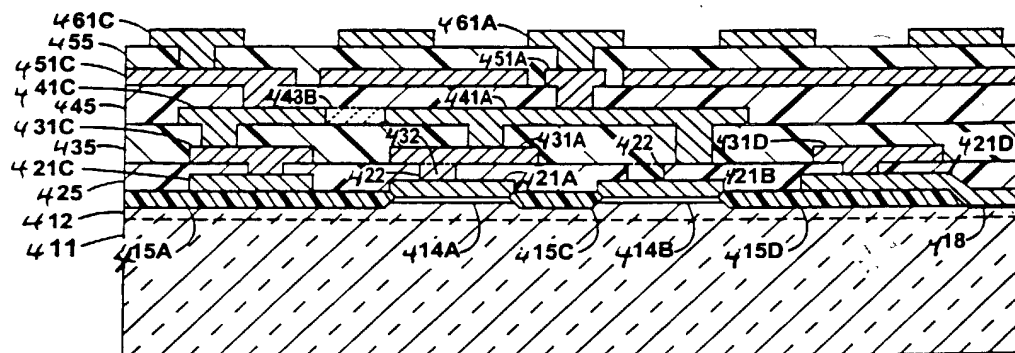




**FIG. 81**



**FIG. 82**



**FIG. 83**

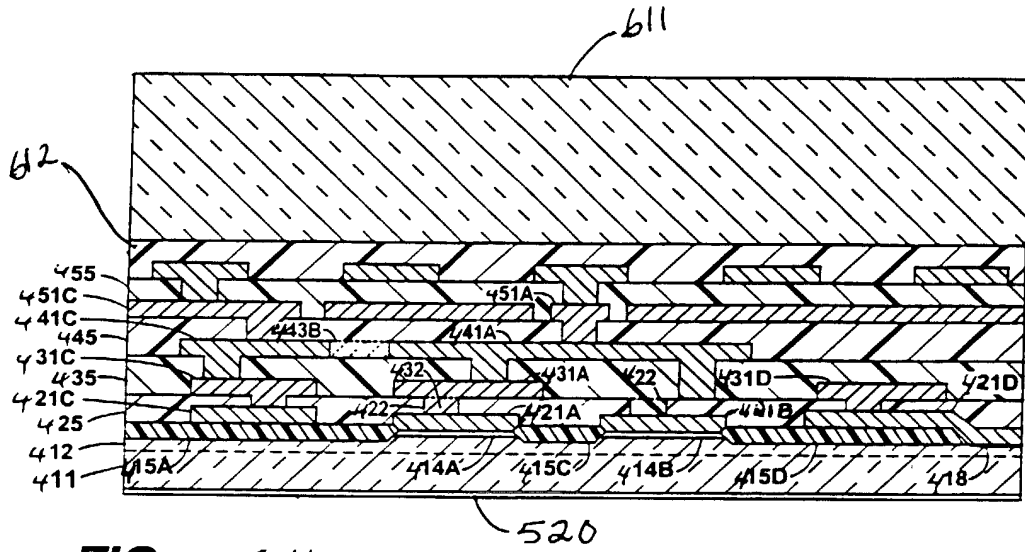


FIG. 84

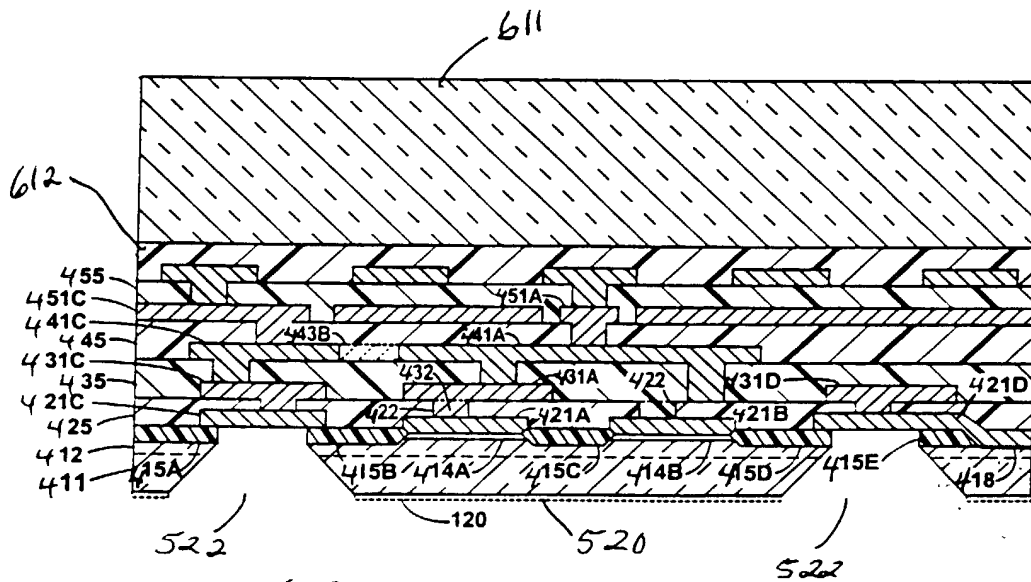
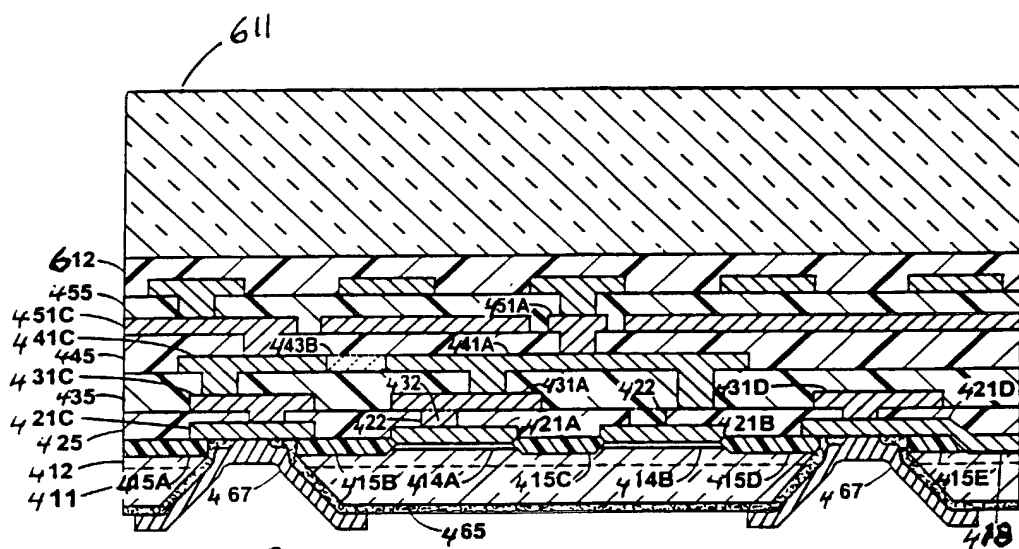
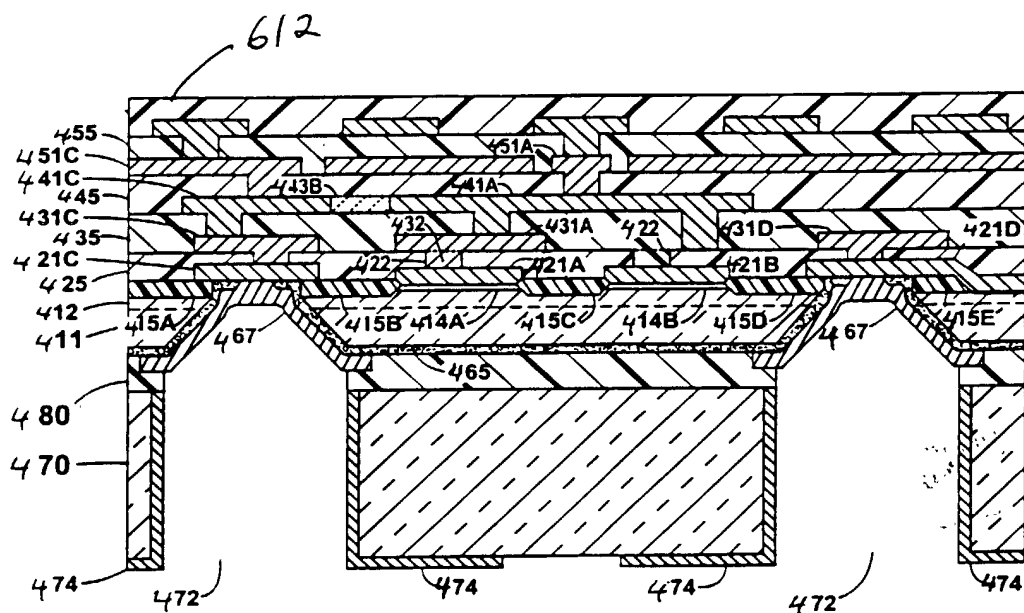


FIG. 85



**FIG. 86**



**FIG. 87**